

# GAOLIN ELECTRONICS

## CPCI 3U14SLOTS BACKPLANE

### Technology Specification

CB31141B0-100

.

Issue Date: 2013-05-18

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### DESIGN ACCORDING TO:

- CPCI Specification PICMG 2.0 R3.0 (October 1, 1999)
- CPCI Hot Swap Specification PICMG 2.1 R1.0 (August 3, 1998)
- CPCI System Management Specification PICMG 2.9 R1.0 (February 2, 2000)
- Keying of CPCI Boards and Backplanes PICMG 2.10 R1.0 (October 1, 1999)
- CPCI Power Interface Specification PICMG 2.11 R1.0 (October1, 1999)

### BUS STRUCTURES:

P2	<b>32bit/33MHz CPCI with Bridge</b>														<b>Three PCIH47 Connectors</b>	
P1																
Slot	1	2	3	4	5	6	7	8	9	10	11	12	13	14		15
SPEC	System	Peripheral														

### TECHNICAL DATA:

- 14 Slots:1 System slot + 13 Peripheral Slots with bridge
- All Peripheral slots support 32bit/33Mhz CPCI bus
- Mechanical dimension: 425.72×128.7×4.0mm (width ×height ×thickness), support 3U card
- PCB Type: 10 layer
- Power connector: Three PCIH47 Power Receptacle
- Maximum voltage drop on backplane power: <20mV
- V(I/O): +3.3V / +5V selectable
- Impedance: 65ohm ±10% for trace
- Operating temperature: -40°C ~ +85°C
- Storage temperature: -55°C ~ +85°C
- MTBF: 700,000h

**PIN ASSIGNMENT:**

See following tables.

**P1 of Slot 1,8**

25	GND	5V	REQ64#	ENUM#	3.3V	5V	GND
24	GND	AD1	5V	V(I/O)	AD0	ACK64#	GND
23	GND	3.3V	AD4	AD3	5V	AD2	GND
22	GND	AD7	GND	3.3V	AD6	AD5	GND
21	GND	3.3V	AD9	AD8	GND	C/BE0#	GND
20	GND	AD12	GND	V(I/O)	AD11	AD10	GND
19	GND	3.3V	AD15	AD14	GND	AD13	GND
18	GND	SERR#	GND	3.3V	PAR	C/BE1#	GND
17	GND	3.3V	IPMB_SCL	IPMB_SDA	GND	PERR#	GND
16	GND	DEVSEL#	GND	V(I/O)	STOP#	LOCK#	GND
15	GND	3.3V	FRAME#	IRDY#	<b>BD_SEL#</b>	TRDY#	GND
14	<b>KEY AREA</b>						
13							
12							
11	GND	AD18	AD17	AD16	GND	C/BE2#	GND
10	GND	AD21	GND	3.3V	AD20	AD19	GND
9	GND	C/BE3#	GND	AD23	GND	AD22	GND
8	GND	AD26	GND	V(I/O)	AD25	AD24	GND
7	GND	AD30	AD29	AD28	GND	AD27	GND
6	GND	REQO#	GND	3.3V	CLK0	AD31	GND
5	GND	BRSVP1A5	BRSVP1B5	RST#	GND	GNT0#	GND
4	GND	IPMB_PWR	HEALTHY#	V(I/O)	INTP	INTS	GND
3	GND	INTA#	INTB#	INTC#	5V	INTD#	GND
2	GND	TCK	5V	TMS	TDO	TDI	GND
1	GND	5V	-12V	TRST#	+12V	5V	GND
<b>Pin</b>	<b>Z</b>	<b>A</b>	<b>B</b>	<b>C</b>	<b>D</b>	<b>E</b>	<b>F</b>

**P1 of Slot 2~7,9~14**

25	GND	5V	REQ64#	ENUM#	3.3V	5V	GND
24	GND	AD1	5V	V(I/O)	AD0	ACK64#	GND
23	GND	3.3V	AD4	AD3	5V	AD2	GND
22	GND	AD7	GND	3.3V	AD6	AD5	GND
21	GND	3.3V	AD9	AD8	M66EN	C/BE0#	GND
20	GND	AD12	GND	V(I/O)	AD11	AD10	GND
19	GND	3.3V	AD15	AD14	GND	AD13	GND
18	GND	SERR#	GND	3.3V	PAR	C/BE1#	GND
17	GND	3.3V	IPMB_SCL	IPMB_SDA	GND	PERR#	GND
16	GND	DEVSEL#	GND	V(I/O)	STOP#	LOCK#	GND
15	GND	3.3V	FRAME#	IRDY#	BD_SEL#	TRDY#	GND
14	<b>KEY AREA</b>						
13							
12							
11	GND	AD18	AD17	AD16	GND	C/BE2#	GND
10	GND	AD21	GND	3.3V	AD20	AD19	GND
9	GND	C/BE3#	GND	AD23	GND	AD22	GND
8	GND	AD26	GND	V(I/O)	AD25	AD24	GND
7	GND	AD30	AD29	AD28	GND	AD27	GND
6	GND	REQ#	GND	3.3V	CLK	AD31	GND
5	GND	BRSVPA15	BRSVP1B5	RST#	GND	GNT#	GND
4	GND	IPMB_PWR	HEALTHY#	V(I/O)	INTP	INTS	GND
3	GND	INTA#	INTB#	INTC#	5V	INTD#	GND
2	GND	TCK	5V	TMS	TDO	TDI	GND
1	GND	5V	-12V	TRST#	+12V	5V	GND
<b>Pin</b>	<b>Z</b>	<b>A</b>	<b>B</b>	<b>C</b>	<b>D</b>	<b>E</b>	<b>F</b>

**P2 of Slot 1,8**

22	GND	GA4	GA3	GA2	GA1	GA0	GND
21	GND	CLK6	GND				GND
20	GND	CLK5	GND				GND
19	GND	GND	GND	SMB_SDA	SMB_SCL	SMB_ALERT	GND
18	GND						GND
17	GND			PRST#	REQ6#	GNT6#	GND
16	GND			DEG#		BRSVP2E16	GND
15	GND			FAL#	REQ5#	GNT5#	GND
14	GND						GND
13	GND						GND
12	GND						GND
11	GND						GND
10	GND						GND
9	GND						GND
8	GND						GND
7	GND						GND
6	GND						GND
5	GND						GND
4	GND	V(I/O)					GND
3	GND	CLK4	GND	GNT3#	REQ4#	GNT4#	GND
2	GND	CLK2	CLK3	GND	GNT2#	REQ3#	GND
1	GND	CLK1	GND	REQ1#	GNT1#	REQ2#	GND
<b>Pin</b>	<b>Z</b>	<b>A</b>	<b>B</b>	<b>C</b>	<b>D</b>	<b>E</b>	<b>F</b>

**P2 of Slot 2~7,9~14**

22	GND	GA4	GA3	GA2	GA1	GA0	GND
21	GND						GND
20	GND						GND
19	GND						GND
18	GND						GND
17	GND						GND
16	GND						GND
15	GND						GND
14	GND						GND
13	GND						GND
12	GND						GND
11	GND						GND
10	GND						GND
9	GND						GND
8	GND						GND
7	GND						GND
6	GND						GND
5	GND						GND
4	GND						GND
3	GND						GND
2	GND						GND
1	GND						GND
<b>Pin</b>	<b>Z</b>	<b>A</b>	<b>B</b>	<b>C</b>	<b>D</b>	<b>E</b>	<b>F</b>

**Optional VI/O Taps (JP1&JP2 ):**

设置 CPCI 总线的 VI/O 电压: +3.3V / +5V selectable, 出厂默认+5V.

**Optional Power Taps :**

The optional power taps are for GND, 5V, +12V.

**PWR\_ON:**

When using ATX power supply, this connector could be used to turn the power supply on if shorted the header of it.

**RESET:**

This connector can reset the system board by shorting the headers of it.

**I2C:**

System Management.

Pin	Signal	Pin	Signal
1	IPMB_PWR	2	SMB_ALERT
3	IPMB_SDA	4	SMB_SDA
5	IPMB_SCL	6	SMB_SCL
7	GND	8	GND
9	GND	10	GND

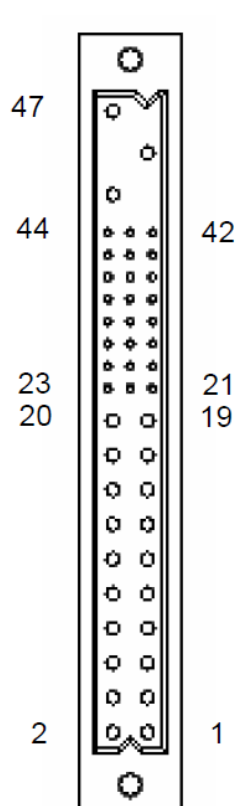
**Volt:**

All the alarm signals and various power supply are connected to it. It is used to connect external monitor module.

Pin	Signal	Pin	Signal
1	-12V	2	PRST#
3	+12V	4	DEG#
5	3.3V	6	FAL#
7	5V	8	GND
9	INH#	10	GND

**INPUT:** 输入端

Pin	Signal	Pin	Signal	Pin	Signal
1	ACL/-DC	2	ACN/+DC	3	CGND

**POWER\_1~3:**


Pin	Signal	Pin	Signal
47	ACL/-DC IN	31	GA2
46	ACN/+DC IN	30	V1 SENSE
45	CGND	29	V1ADJ
44	V3 SHARE	28	GA1
43	IPMB_PWR	27	EN#
42	+FAL#	26	RESERVED
41	V2 SHARE	25	GA0
40	IPMB_SDA	24	RTN
39	INH#	23	RESERVED
38	DEG#	22	RTN
37	IPMB_SCL	21	V4
36	V3 SENSE	20	V3
35	V1 SHARE	19	RTN
34	S RTN	13-18	V2
33	V2 SENSE	5-12	RTN
32	V2ADJ	1-4	V1

**NOTE:**

The position from slot 5 to slot 8 can't support rear-plane I/Os , as the segment is occupied by the bridge.



# BACKPLANE TOP VIEW

