

GAOLIN ELECTRONICS

CPCI 3U21SLOTS BACKPLANE

Technology Specification

CB31213B0-100

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DESIGN ACCORDING TO:

- CPCI Specification PICMG 2.0 R3.0 (October 1, 1999)
- CPCI Hot Swap Specification PICMG 2.1 R1.0 (August 3, 1998)
- CPCI System Management Specification PICMG 2.9 R1.0 (February 2, 2000)
- Keying of CPCI Boards and Backplanes PICMG 2.10 R1.0 (October 1, 1999)
- CPCI Power Interface Specification PICMG 2.11 R1.0 (October1, 1999)

BUS STRUCTURES:

P2	32Bit/33MHz CPCI withBridge																				
P1																					
Slot	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
SPEC	System	Peripheral																			

TECHNICAL DATA:

- 21 Slots:1 System slot + 20 Peripheral Slots with bridge
- All Peripheral slots support 32bit/33Mhz CPCI bus
- Mechanical dimension: 425.72 ×128.7 ×3.8mm(width ×height ×thickness), support 3U card
- PCB Type: 10 layer
- Power connector: ATX Power Receptacle
- Maximum voltage drop on backplane power: <20mV
- V(I/O): +3.3V / +5V selectable
- Impedance: 65ohm ±10% for trace
- Operating temperature: -40°C ~ +85°C
- Storage temperature: -55°C ~ +85°C
- MTBF: 700,000h

PIN ASSIGNMENT:

See following tables.

P1 of Slot 1, 8, 15

25	GND	5V	REQ64#	ENUM#	3.3V	5V	GND
24	GND	AD1	5V	V(I/O)	AD0	ACK64#	GND
23	GND	3.3V	AD4	AD3	5V	AD2	GND
22	GND	AD7	GND	3.3V	AD6	AD5	GND
21	GND	3.3V	AD9	AD8	GND	C/BE0#	GND
20	GND	AD12	GND	V(I/O)	AD11	AD10	GND
19	GND	3.3V	AD15	AD14	GND	AD13	GND
18	GND	SERR#	GND	3.3V	PAR	C/BE1#	GND
17	GND	3.3V	IPMB_SCL	IPMB_SDA	GND	PERR#	GND
16	GND	DEVSEL#	GND	V(I/O)	STOP#	LOCK#	GND
15	GND	3.3V	FRAME#	IRDY#	GND	TRDY#	GND
14	KEY AREA						
13							
12							
11	GND	AD18	AD17	AD16	GND	C/BE2#	GND
10	GND	AD21	GND	3.3V	AD20	AD19	GND
9	GND	C/BE3#	GND	AD23	GND	AD22	GND
8	GND	AD26	GND	V(I/O)	AD25	AD24	GND
7	GND	AD30	AD29	AD28	GND	AD27	GND
6	GND	REQ0#	GND	3.3V	CLK0	AD31	GND
5	GND	BRSVP1A5	BRSVP1B5	RST#	GND	GNT0#	GND
4	GND	IPMB_PWR	HEALTHY#	V(I/O)	INTP	INTS	GND
3	GND	INTA#	INTB#	INTC#	5V	INTD#	GND
2	GND	TCK	5V	TMS	TDO	TDI	GND
1	GND	5V	-12V	TRST#	+12V	5V	GND
Pin	Z	A	B	C	D	E	F

P1 of Slot 2~7, 9~14, 16~21

25	GND	5V	REQ64#	ENUM#	3.3V	5V	GND
24	GND	AD1	5V	V(I/O)	AD0	ACK64#	GND
23	GND	3.3V	AD4	AD3	5V	AD2	GND
22	GND	AD7	GND	3.3V	AD6	AD5	GND
21	GND	3.3V	AD9	AD8	M66EN	C/BE0#	GND
20	GND	AD12	GND	V(I/O)	AD11	AD10	GND
19	GND	3.3V	AD15	AD14	GND	AD13	GND
18	GND	SERR#	GND	3.3V	PAR	C/BE1#	GND
17	GND	3.3V	IPMB_SCL	IPMB_S	GND	PERR#	GND
16	GND	DEVSEL#	GND	V(I/O)	STOP#	LOCK#	GND
15	GND	3.3V	FRAME#	IRDY#	BD_SEL#	TRDY#	GND
14	KEY AREA						
13							
12							
11	GND	AD18	AD17	AD16	GND	C/BE2#	GND
10	GND	AD21	GND	3.3V	AD20	AD19	GND
9	GND	C/BE3#	IDSEL	AD23	GND	AD22	GND
8	GND	AD26	GND	V(I/O)	AD25	AD24	GND
7	GND	AD30	AD29	AD28	GND	AD27	GND
6	GND	REQ#	GND	3.3V	CLK	AD31	GND
5	GND	BRSVPA15	BRSVP1B5	RST#	GND	GNT#	GND
4	GND	IPMB_PWR	HEALTHY#	V(I/O)	INTP	INTS	GND
3	GND	INTA#	INTB#	INTC#	5V	INTD#	GND
2	GND	TCK	5V	TMS	TDO	TDI	GND
1	GND	5V	-12V	TRST#	+12V	5V	GND
Pin	Z	A	B	C	D	E	F

备注：中断信号

	Slot #	IDSEL	A3	B3	C3	E3
BUS#1	1 (SYS)	-	INTA#	INTB#	INTC#	INTD#
	2	AD31	INTD#	INTA#	INTB#	INTC#
	3	AD30	INTC#	INTD#	INTA#	INTB#
	4	AD29	INTB#	INTC#	INTD#	INTA#
	5	AD25	INTB#	INTC#	INTD#	INTA#
	6	AD27	INTD#	INTA#	INTB#	INTC#
	7	AD26	INTC#	INTD#	INTA#	INTB#
BUS#2	8	AD28	INTA#	INTB#	INTC#	INTD#
	9	SAD31	INTD#	INTA#	INTB#	INTC#
	10	SAD30	INTC#	INTD#	INTA#	INTB#
	11	SAD29	INTB#	INTC#	INTD#	INTA#
	12	SAD25	INTB#	INTC#	INTD#	INTA#
	13	SAD27	INTD#	INTA#	INTB#	INTC#
	14	SAD26	INTC#	INTD#	INTA#	INTB#
BUS#3	15	SAD28	INTA#	INTB#	INTC#	INTD#
	16	TAD31	INTD#	INTA#	INTB#	INTC#
	17	TAD30	INTC#	INTD#	INTA#	INTB#
	18	TAD29	INTB#	INTC#	INTD#	INTA#
	19	TAD28	INTA#	INTB#	INTC#	INTD#
	20	TAD27	INTD#	INTA#	INTB#	INTC#
	21	TAD26	INTC#	INTD#	INTA#	INTB#

P2 of Slot 1

22	GND	GA4	GA3	GA2	GA1	GA0	GND
21	GND	CLK6	GND				GND
20	GND	CLK5	GND				GND
19	GND	GND	GND	SMB_SDA	SMB_SCL	SMB_ALERT	GND
18	GND						GND
17	GND			PRST#	REQ6#	GNT6#	GND
16	GND			DEG#			GND
15	GND			FAL#	REQ5#	GNT5#	GND
14	GND						GND
13	GND						GND
12	GND						GND
11	GND						GND
10	GND						GND
9	GND						GND
8	GND						GND
7	GND						GND
6	GND						GND
5	GND						GND
4	GND	V(I/O)					GND
3	GND	CLK4	GND	GNT3#	REQ4#	GNT4#	GND
2	GND	CLK2	CLK3	GND	GNT2#	REQ3#	GND
1	GND	CLK1	GND	REQ1#	GNT1#	REQ2#	GND
Pin	Z	A	B	C	D	E	F

P2 of Slot 8, 15

22	GND	GA4	GA3	GA2	GA1	GA0	GND
21	GND	CLK6	GND				GND
20	GND	CLK5	GND				GND
19	GND	GND	GND				GND
18	GND						GND
17	GND				REQ6#	GNT6#	GND
16	GND				GND		GND
15	GND				REQ5#	GNT5#	GND
14	GND						GND
13	GND						GND
12	GND						GND
11	GND						GND
10	GND						GND
9	GND						GND
8	GND						GND
7	GND						GND
6	GND						GND
5	GND						GND
4	GND	VI/O					GND
3	GND	CLK4	GND	GNT3#	REQ4#	GNT4#	GND
2	GND	CLK2	CLK3		GNT2#	REQ3#	GND
1	GND	CLK1	GND	REQ1#	GNT1#	REQ2#	GND
Pin	Z	A	B	C	D	E	F

P2 of Slot 2~7, 9~14, 16~21

22	GND	GA4	GA3	GA2	GA1	GA0	GND
21	GND						GND
20	GND						GND
19	GND						GND
18	GND						GND
17	GND						GND
16	GND						GND
15	GND						GND
14	GND						GND
13	GND						GND
12	GND						GND
11	GND						GND
10	GND						GND
9	GND						GND
8	GND						GND
7	GND						GND
6	GND						GND
5	GND						GND
4	GND						GND
3	GND						GND
2	GND						GND
1	GND						GND
Pin	Z	A	B	C	D	E	F

Optional Power Taps :

The optional power taps are for +3.3V, +5V, +12V, -12V, GND。

VI/O Taps (JP1&JP2):

设置 CPCI 总线的 VI/O 电压: +3.3V / +5V 可选, 出厂默认+5V。

PWR_ON: (开关插座, 出厂默认短接)

Pin	Signal	Pin	Signal
1	PWR_ON#	2	GND

RESET: (复位插座)

Pin	Signal	Pin	Signal
1	PRST#	2	GND

LED: (指示灯插座)

Pin	Signal	Pin	Signal
1	+12V	2	GND

Volt: (电压插座)

Pin	Signal	Pin	Signal
2	PRST#	1	-12V
4	DEG#	3	+12V
6	FAL#	5	3.3V
8	GND	7	5V
10	GND	9	PS_ON#

注意:

第 5~8 槽和第 12~15 槽位置背部 J1, J2 位置连接器被桥接模块占用, 所以不支持 RI/O, 此产品最多支持 13 个 RI/O。

